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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of: Ajit V. Sathe et al.

Title: SOLDERLESS ELECTRONICS PACKAGING AND METHODS OF MANUFACTURE

Attorney Docket No.: 884.341US1

PATENT APPLICATION TRANSMITTAL

BOX PATENT APPLICATION

Commissioner for Patents
Washington, D.C. 20231

We are transmitting herewith the following attached items and information (as indicated with an "X"):

- ☒ Return postcard.
☒ Utility Patent Application under 37 CFR § 1.53(b) comprising:
☒ Specification (31 pgs, including claims numbered 1 through 58 and a 1 page Abstract).
☒ Formal Drawing(s) (14 sheets).
☒ Signed Combined Declaration and Power of Attorney (6 pgs).
☒ Check in the amount of \$1,794.00 to pay the filing fee.
☒ Assignment of the invention to Intel Corporation (4 pgs) and Recordation Form Cover Sheet.
☒ Check in the amount of \$40.00 to pay the Assignment recording fee.
☒ Information Disclosure Statement (1 pgs), Form 1449 (1 pgs), and copies of cited references (7).

The filing fee has been calculated below as follows:

	No. Filed	No. Extra	Rate	Fee
TOTAL CLAIMS	58 - 20 =	38	x 18 =	\$684.00
INDEPENDENT CLAIMS	8 - 3 =	5	x 80 =	\$400.00
[] MULTIPLE DEPENDENT CLAIMS PRESENTED				\$0.00
BASIC FEE				\$710.00
TOTAL				\$1,794.00

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